

Figs. 4A and 4B are cross sectional views of a TFT according to the example 1;

Figs. 7A, and 7B are top views and a cross section view of an EL display panel according to the example 3;

Figs. 8A and 8B are cross sectional views of an EL display panel according to the example 3;

Figs. 10A, 10B, 10C, 10D, 10E and 10F are views showing a variety of semiconductor devices according to the example 4;

Figs. 11A, 11B, 11C and 11D are views showing a variety of semiconductor devices according to the example 4; and

Figs. 12A, 12B and 12C are views showing a variety of semiconductor devices according to the example 4.

FIG. 10A